## Notice of References Cited Application/Control No. Applicant(s)/Patent Under Reexamination ALON ET AL. Examiner Art Unit Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5,883,814	03-1999	Luk et al.	716/2
	В	US-2002/0095648	07-2002	Saito, Tatsuhito	716/10
	С	US-6,381,563	04-2002	O'Riordan et al.	703/14
	D	US-2004/0148578	07-2004	McConaghy et al.	716/001
	E	US-2004/0172609	09-2004	Hassibi et al.	716/018
	F	US-6,760,900	07-2004	Rategh et al.	716/19
	G	US-6,360,191	03-2002	Koza et al.	703/6
	н	US-5,959,871	09-1999	Pierzchala et al.	703/4
	ı	US-6,557,145	04-2003	Boyle et al.	716/2
	J	US-			
	К	US-			
	L	US-			
	М	US-			

## **FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	2					
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	Ρ					
	ø		,			
	R					
	s					
	T					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	υ	R. Beale et al., "Design Methodology and Simulation Tools for Mixed Analog-Digital Integrated Circuits", IEEE International Systems on Circuits and Systems, pp. 1351-1355, May, 1990.
	٧	
	w	
	х	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707,05(a).)

Dates in MM-YYYYY format are publication dates. Classifications may be US or foreign.